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In this Fourth Edition of ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS, Paul Gray and Robert Meyer have teamed up with two new coauthors-Paul Hurst

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in High Temperature
Analog CMOS
Integrated Circuits
Abstract: The design of
CMOS analog
integrated circuits to
be operated at
elevated junction
temperatures is
discussed.

Considerations which
have successfully been
implemented in the

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integrated circuits that
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engineering, University
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December 18,

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The fundamentals are covered in great detail--and the basics are still the basics. Nevertheless, a few things have changed. For instance, integrated inductors (a mainstay of CMOS RF ICs) are casually dismissed as impractical. But all in all, a very fine text. Well worth reading for anyone serious about analog design.

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CMOS technology. With
the supply voltage of
3.3 V, the circuit
maintains a constant
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